Tantalo Selected

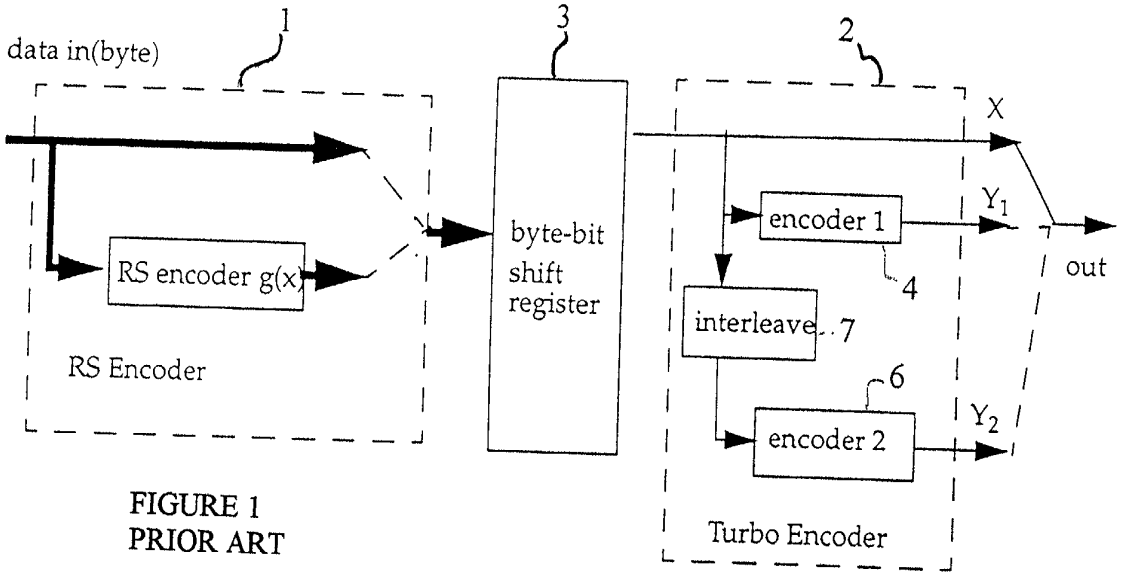

FIGURE 1
PRIOR ART

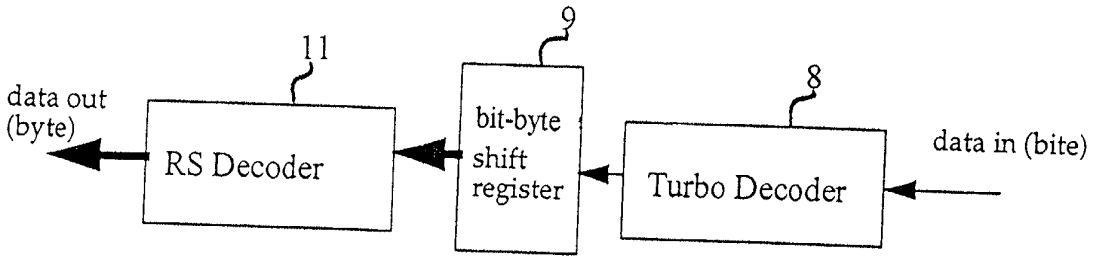

FIGURE 2
PRIOR ART

TOSTSD-SSTS2CSD

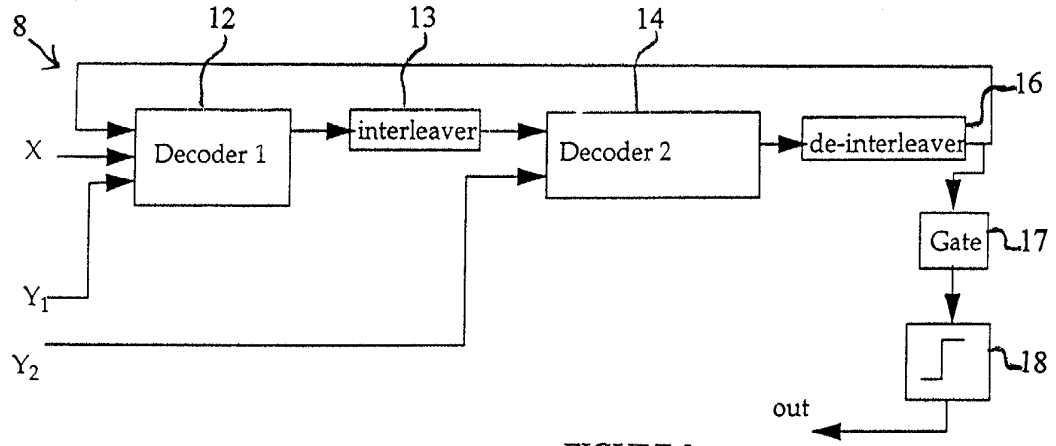

FIGURE 3
PRIOR ART

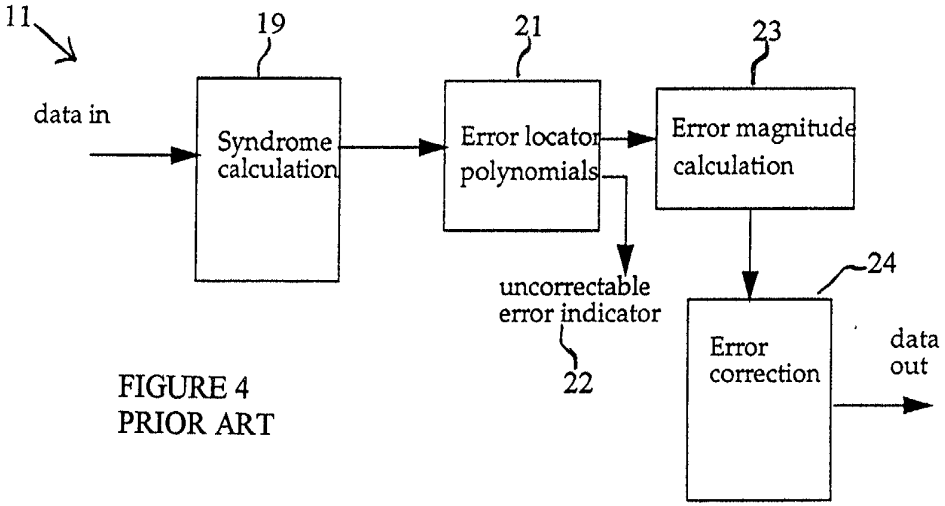

FIGURE 4
PRIOR ART

Tuotteen sensored

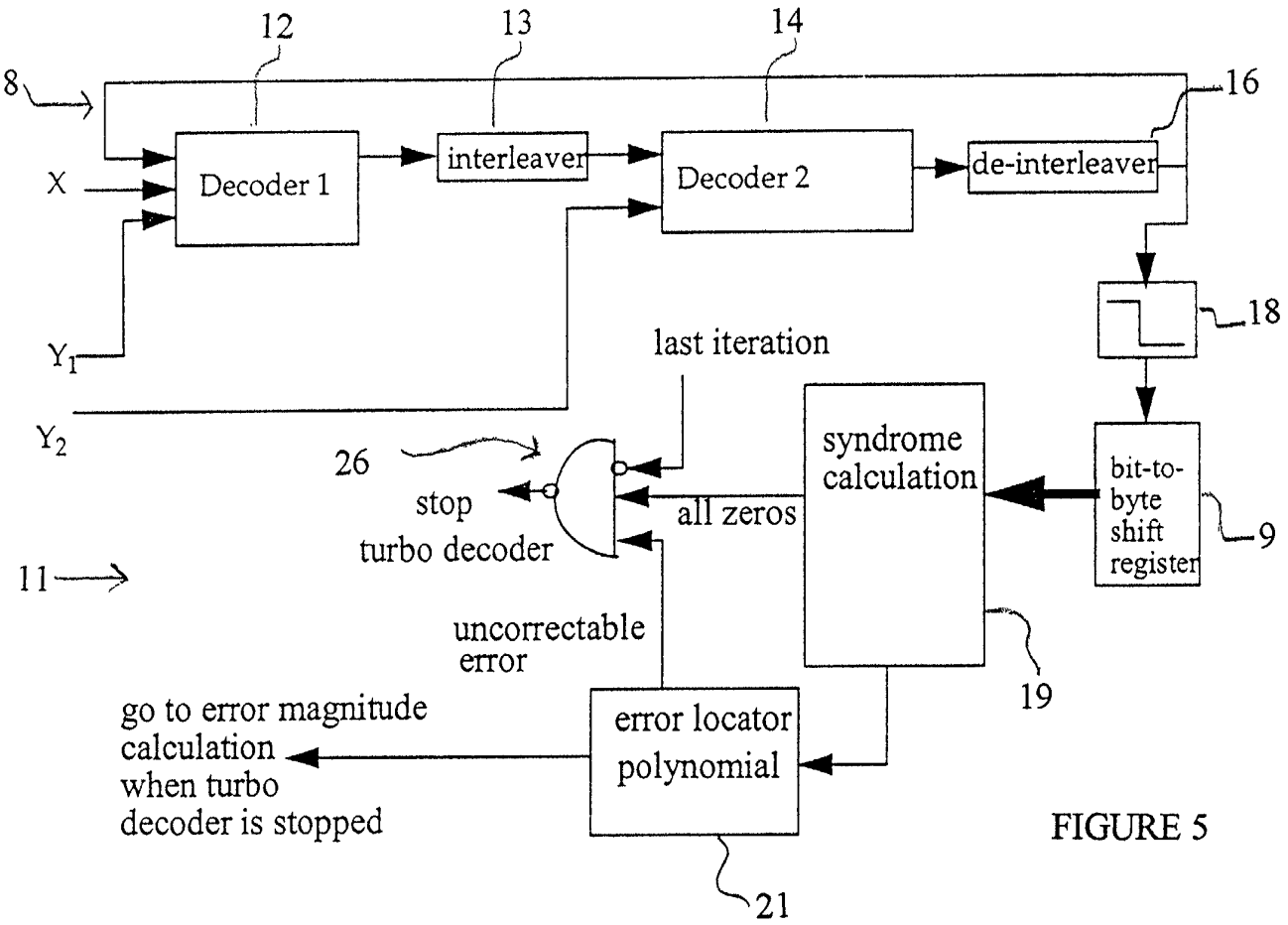

FIGURE 5

Tomas Samsaeg

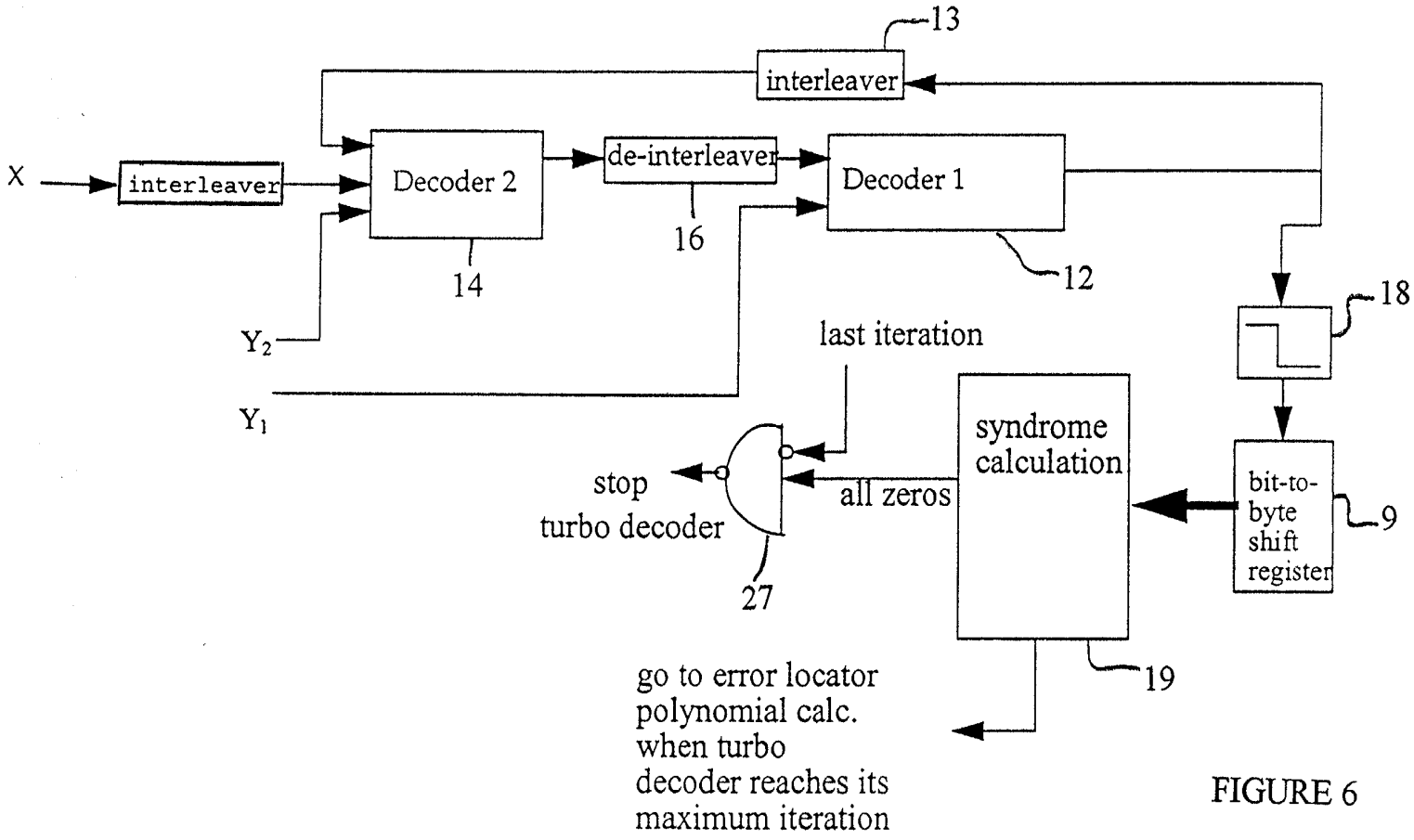

FIGURE 6

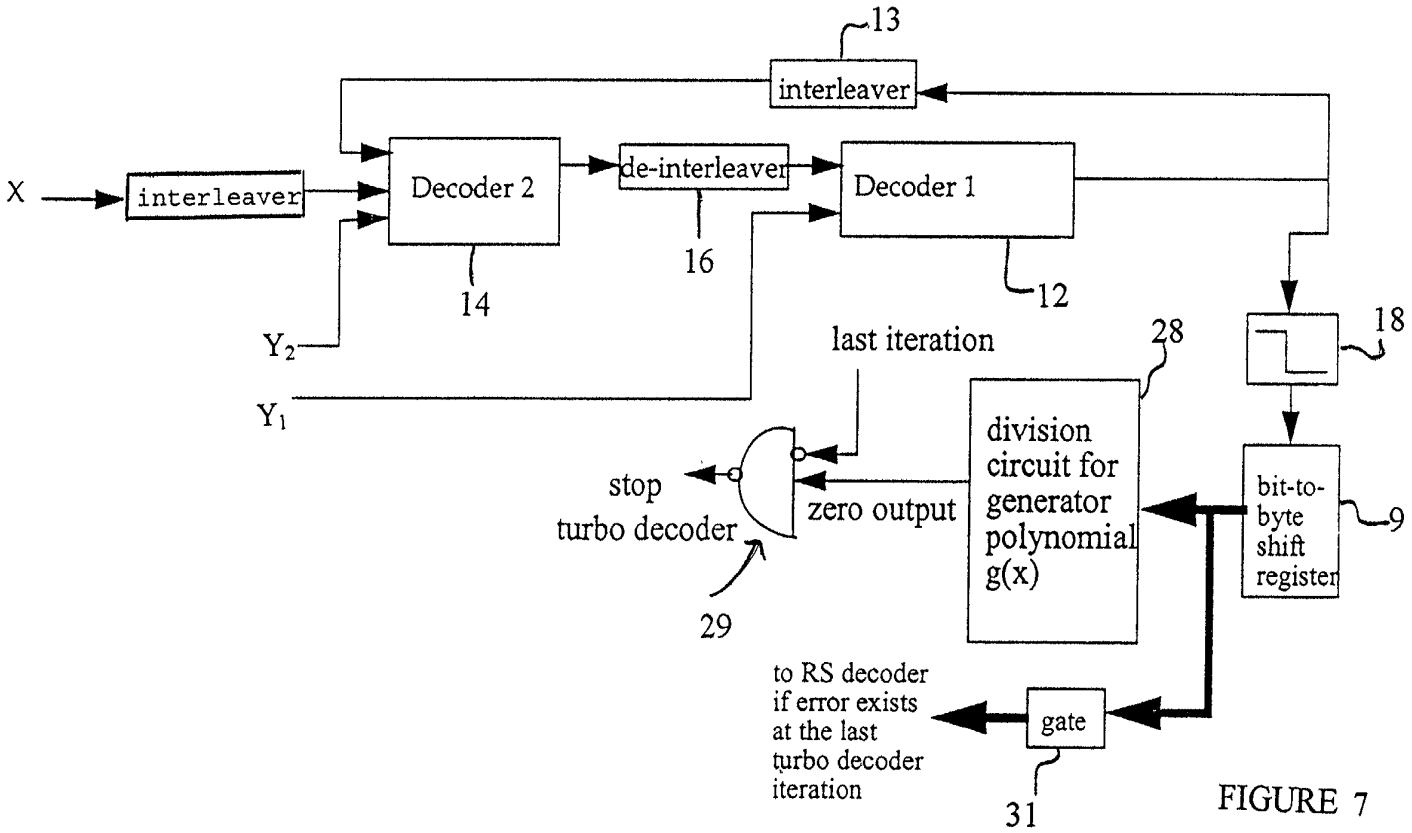
13
interleaver
X
interleaver
Decoder 2
de-interleaver
Decoder 1
14
16
12
Y_2
Y_1
last iteration
28
18
division circuit for generator polynomial g(x)
stop turbo decoder
zero output
bit-to-byte shift register
9
29
to RS decoder if error exists at the last turbo decoder iteration
gate
31

FIGURE 7